

AMENDMENTS TO THE CLAIMS:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-3. (Cancelled).

4. (Currently amended) [[A]] ~~The process according to claim [[3]] 19, wherein the stored information is an interrupt generated by a phase-forming supply controller generates one-interruption-to-an-interruption-controller, in case of based on~~ a variation in availability of at least one ~~resource~~ of said power supplies.

5. (Currently amended) [[A]] ~~The process according to claim [[3]] 4, wherein the supply controller phase is capable of selectively generating an interruption in the course of transitions from one status to another, more particularly in case~~ generates the interrupt in the event of:

- a transition from a state of low consumption to supply via the contactless interface[:]] device, wherein the interrupt is generated when interruption-occurs if the voltage via this said contactless interface device is higher than [[the]] a threshold voltage;
- a transition from supply via the contactless interface to the cessation of [[this]] the supply, [[:]] wherein the interrupt is generated interruption-occurs when the voltage received by the contactless interface device is lower than the threshold voltage;
- a transition from supply via the contactless interface device to supply via the contact interface device; the interruption-occurs; and

- ~~a transition~~ Transition or reset sequence commanded by the contact interface device,
with supply via and the power source is provided to the contact interface, ~~the interruption~~
~~occurs~~.

6. (Currently amended) [[A]] The process according to claim 5, wherein, during said
transition from supply via the contactless interface to the cessation of [[this]] ~~the supply+the~~
~~interruption occurs when the voltage received by the contactless interface is lower than a~~
~~threshold voltage; in the wake~~, the [[chip]] object is placed in a sleep state.

7. (Currently amended) [[A]] The process according to, claim 5, wherein the value of
the critical threshold voltage is predetermined, said ~~in such a way as to allow risk-free~~
~~transfer of complete cessation of the supply of the chip; for example, the value of this~~
threshold voltage [[is]] being slightly higher than a minimum voltage for operation of the
[[chip]] object.

8. (Currently amended) [[A]] The process according to claim [[1]] 19, wherein [[this]]
the process comprises at least one step of immediate warning for fully simultaneous
management of power [[and/or]] or clock resources.

9-12. (Cancelled).

13. (Currently amended) The [[A]] device according to the claim [[12]] 23, wherein the
[[said]] device includes means of immunity comprising, ~~at least the functional block forming~~
a power supply controller[[,]] which detects the appearance [[and/or]] or the disappearance of
~~resources~~ the power supplies.

14. (Currently amended) The [[A]] device according to the claim 13, wherein the means of immunity ~~comprise~~ includes, means for the fully simultaneous management of power, [[and/or]] clock [[and/or]] or delay resources.

15. (Currently amended) The [[A]] device according to claim [[12]] 23, further comprising means for providing [[of]] immediate warning, ~~for fully simultaneous management of changes in power supply~~ [[and/or]] or clock resources.

16. (Currently amended) The [[A]] device according to claim [[12]] 23, wherein [[this]] the device ~~is configured to delay the makes provision for at least one step of transaction maintenance, with at least one phase of delaying simulation of resetting~~ [[,]] ordered by the contact interface during a transition aiming to reinitialise the [[chip]] object during a change ~~in course of the resources the power supplies~~.

17. (Currently amended) The [[A]] device according to the claim 16, ~~comprising wherein this functional block comprises wiring or similar for supply of the chip~~ means for supplying the object with appropriate voltage and power, ~~for information of this chip the object information regarding~~ [[of]] the appearance [[and/or]] or disappearance of supply-resources voltage derived from the contact interface [[and/or]] or the contactless interface.

18. (Currently amended) The [[A]] device according to claim 16, wherein [[this]] the device comprises a functional block forming a sleep controller, said sleep controller controlling the object to conform ~~for conformation of the chip~~ to constraints of low consumption during sleep states.

19. (New) A process for allowing simultaneous functioning of a contact interface device and a contactless interface device in an intelligent portable data object, wherein:

said contact interface device and said contactless interface device have respective power supplies that include a plurality of operating states, and

said intelligent portable data object is configured to reset in the event of simultaneous operation of both of said interfaces,

said process including:

in the event of a change in state of one or more of said power supplies, storing in a memory device information corresponding to the change in state; and

based on the stored state information, preventing the reset of the portable data object by the contact interface.

20. (New) The process of claim 19, wherein preventing the reset includes delaying the reset for a predefined period of time.

21. (New) The process of claim 19, wherein preventing the reset includes simulating the reset.

22. (New) A device for allowing simultaneous functioning of a contact interface and a contactless interface in an intelligent portable data object that is otherwise configured to reset in the event of simultaneous operation of said interfaces, said system comprising

means for storing information corresponding to the change in state in the event of a change in state of one or more said power supplies corresponding, respectively, to said interfaces; and

means for preventing the reset of the portable data object by the contact interface based on the stored state information.

23. (New) The device of claim 22, wherein the means for preventing the reset delays the reset for a predefined period of time.

24. (New) The device of claim 22, wherein the means for preventing the reset simulates the reset.